

further view of Guetz et al. Based on the following, these rejections are respectfully traversed.

Previously, the Applicant has respectfully pointed out that the claims recite features not taught by Ueno et al. In particular such features include "determining values of additional pixels based on values of pixels in the first block and on values of pixels in the one or more blocks", as recited in claims 1, 14, 27 and 40. However, despite this point, the above rejections have been maintained.

In maintaining the above rejections, it was stated now that this feature is disclosed in column 8, line 41 to column 9, line 34, column 12, line 35 to column 13, line 60, column 15, lines 33-41, and column 16, lines 8-20, of Ueno et al. However, after carefully reviewing these portions of Ueno et al., the Applicant still does not see where such a feature is disclosed.

Further, in maintaining these rejections, the Examiner appears to be relying on the vertical interpolation disclosed in column 19, lines 41-54, of Ueno et al. However, in this portion, Ueno et al. only discloses that an odd field signal and even field signal are subjected to a vertical interpolation. Based on this disclosure, it is evident that individual fields are only being interpolated. Thus, it is unreasonable to interpret this as "determining

values of additional pixels based on values of pixels in the first block and on values of pixels in the one or more blocks", as required by the claims. Therefore, it is respectfully submitted that this feature is not anticipated by Ueno et al.

The above-described deficiencies of Ueno et al. are also not addressed by either Lempel or Guetz et al. since they are being relied on for other features. Thus, the invention of Claims 1-7, 9-20, 22-33, 35-40 and 45-47 is neither anticipated nor made obvious by Ueno et al. alone or in combination with either Lempel or Guetz et al. Therefore, it is respectfully requested that these rejections be reconsidered and withdrawn.

Claims 42-43 stand rejected under 35 USC 102 as being anticipated by Yonemitsu et al. Claim 44 stands rejected under 35 USC 103 as being unpatentable over Yonemitsu et al. in view of Song et al. Based on the following, these rejections are respectfully traversed.

Previously, the Applicant has respectfully pointed out that the claims recite features not taught by Yonemitsu et al. In particular such features include "a processor which increases a resolution of a reference frame of the video based on pixels in the reference frame and based on pixels in at least one other target frame of the video". However,

despite this point, the above rejections have been maintained.

In maintaining the above rejections, it was stated now that the interpolation process provided within the upsampling circuit 57 provides the same increase in resolution of a reference frame of the video based on pixels in the reference frame and at least one other target frame.

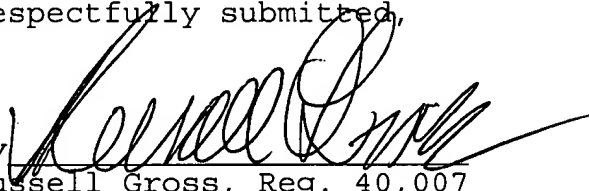
However, in column 7, lines 11-13, Yanemitsu et al. clearly discloses that the output picture S66 of the lower layer is input to the up sampling circuit 57, in which it is processed by interpolation so that its converted into a non-interlace picture. Based on the above disclosure, it is evident that only a single picture is interpolated. Thus, it is evident that the up sampling circuit 57 of Yonemitsu et al. does not "increase a resolution of a reference frame of the video based on pixels in the reference frame and based on pixels in at least one other target frame of the video", as required by the claims. Therefore, it is respectfully submitted that this feature is not anticipated by Yonemitsu et al.

The above-described deficiencies of Yonemitsu et al. are also not addressed by Song et al. Thus, the invention of claims 42-44 is neither anticipated nor made Yonemitsu

et al. alone or in combination with Song et al. Therefore,
it is respectfully requested that these rejections be
reconsidered and withdrawn.

The Commissioner is hereby authorized to credit any
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Respectfully submitted,

By 
Russell Gross, Reg. 40,007
Attorney
(914) 333-9631
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By G. Sengrecht